

## 800V 4A 3.5Ω N-ch Power MOSFET

### Description

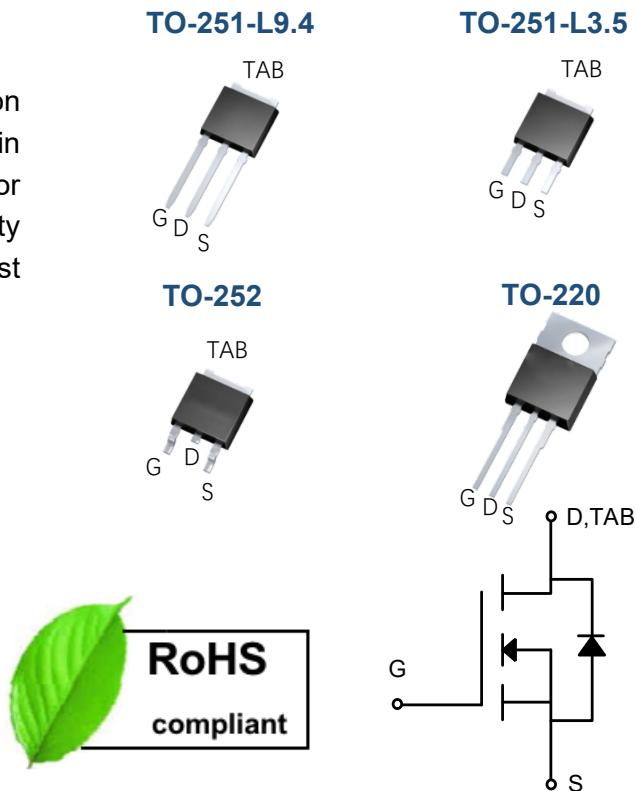
WMOS™ D1 is Wayon's 1<sup>st</sup> generation VDMOS family that is dramatic reduction in on-resistance and ultra-low gate charge for applications requiring high power density and high efficiency. And it is very robust and RoHS compliant.

### Features

- Typ. $R_{DS(on)}$ =3.5Ω@ $V_{GS}=10V$
- 100% avalanche tested
- Pb-free, Halogen free

### Applications

- SMPS
- Charger
- DC-DC



### Absolute Maximum Ratings ( $T_C=25^{\circ}C$ )

Parameter	Symbol	WMAA/WMH/WMO	WMK	Unit
Drain-source voltage	$V_{DSS}$	800		V
Gate-source voltage	$V_{GS}$	$\pm 30$		V
Continuous drain current	$I_D$	4		A
Pulsed drain current <sup>1</sup>	$I_{DM}$	16		A
Avalanche energy, single pulse <sup>2</sup>	$E_{AS}$	24		mJ
Power dissipation	$P_D$	96	114	W
Derate above 25°C		0.8	0.9	W/°C
Operating junction temperature	$T_j$	-55~150		°C
Storage temperature	$T_{stg}$	-55~150		°C
Continuous diode forward current	$I_S$	4		A
Diode pulse current	$I_{Spulse}$	16		A

### Thermal Characteristic

Thermal resistance,junction-to-case	$R_{\theta JC}$	1.3	1.1	°C/W
Thermal resistance,junction-to-ambient	$R_{\theta JA}$	62.5	62.5	°C/W

**Electrical Characteristics of MOSFET**

				T <sub>C</sub> =25°C	Min.	Typ.	Max.
Drain-source break down voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250uA, V <sub>GS</sub> =0V		T <sub>J</sub> =25°C	800	-	-
Gate threshold voltage	V <sub>GS(th)</sub>	I <sub>D</sub> =250uA, V <sub>DS</sub> =V <sub>GS</sub>		T <sub>J</sub> =25°C	2.0	3.1	4.0
Drain-source leakage current	I <sub>DSS</sub>	V <sub>DS</sub> =800V, V <sub>GS</sub> =0V		T <sub>J</sub> =25°C	-	-	1
		V <sub>DS</sub> =720V, V <sub>GS</sub> =0V		T <sub>J</sub> =125°C	-	-	100
Gate-source leakage current,forward	I <sub>GSSF</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =30V		T <sub>J</sub> =25°C	-	-	100
Gate-source leakage current,reverse	I <sub>GSSR</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =-30V		T <sub>J</sub> =25°C	-	-	-100
Drain-source on-state resistance <sup>3</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =2A		T <sub>J</sub> =25°C	-	3.5	4

**Dynamic Characteristics of MOSFET (T<sub>C</sub>=25°C)**

					Min.	Typ.	Max.
Input capacitance	C <sub>iss</sub>	f=1MHz, V <sub>DS</sub> =25V, V <sub>GS</sub> =0V			-	497	-
Output capacitance	C <sub>oss</sub>				-	57	-
Reverse transfer capacitance	C <sub>rss</sub>				-	6	-
Gate to source charge	Q <sub>gs</sub>	V <sub>DD</sub> =400V I <sub>D</sub> =4A V <sub>GS</sub> = 0 to 10V			-	3	-
Gate to drain charge	Q <sub>gd</sub>				-	5	-
Total gate charge	Q <sub>g</sub>				-	13	-

**Switching Characteristics of MOSFET (T<sub>C</sub>=25°C)**

					Min.	Typ.	Max.
Turn-on delay time	t <sub>d on</sub>	V <sub>DS</sub> =400V, I <sub>D</sub> =4A, R <sub>G</sub> =10Ω, V <sub>GS</sub> =0 to 10V			-	10	-
Rise time	t <sub>r</sub>				-	25	-
Turn-off delay time	t <sub>d off</sub>				-	37	-
Fall time	t <sub>f</sub>				-	27	-

**Characteristics of Body Diode (T<sub>C</sub>=25°C)**

					Min.	Typ.	Max.
Forward voltage	V <sub>SD</sub>	I <sub>SD</sub> =4A, V <sub>GS</sub> =0V			-	-	1.4
Reverse recovery time	t <sub>rr</sub>	I <sub>s</sub> =4A, V <sub>GS</sub> =0V di/dt=100A/us			-	206	-
Reverse recovery current	I <sub>rr</sub>				-	5.6	-
Recovery charge	Q <sub>rr</sub>				-	0.6	-

Notes:

1. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub> =150°C.
2. The EAS data shows Max. rating . The test condition is V<sub>DD</sub> =50V, V<sub>GS</sub> =10V, L=10mH, I<sub>AS</sub> =2.2A, T<sub>C</sub>=25°C.
3. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.

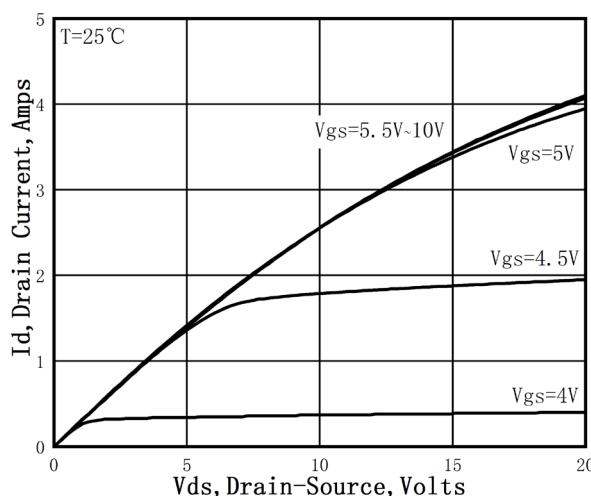


Figure 1. On-Region Characteristics

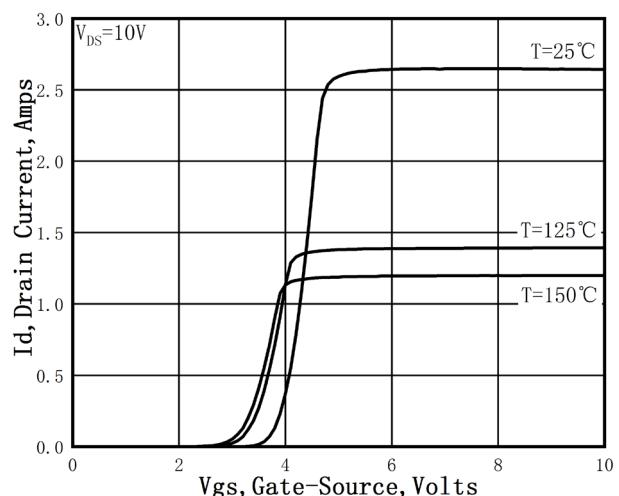


Figure 2. Transfer Characteristics

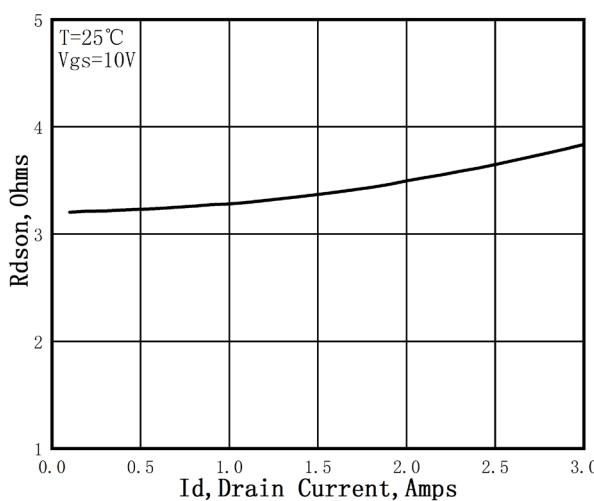


Figure 3. Static Drain-Source On Resistance

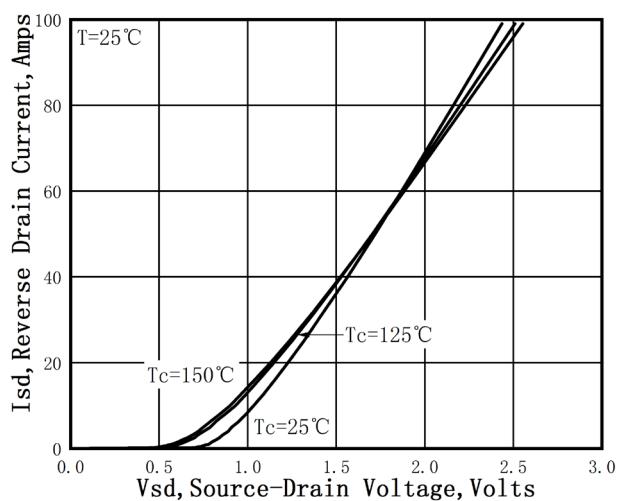
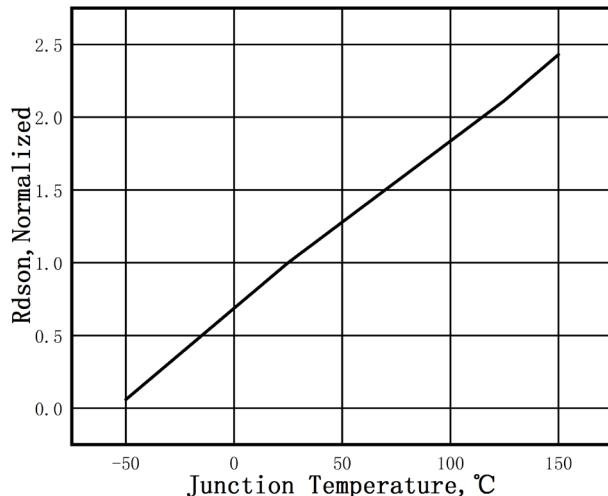
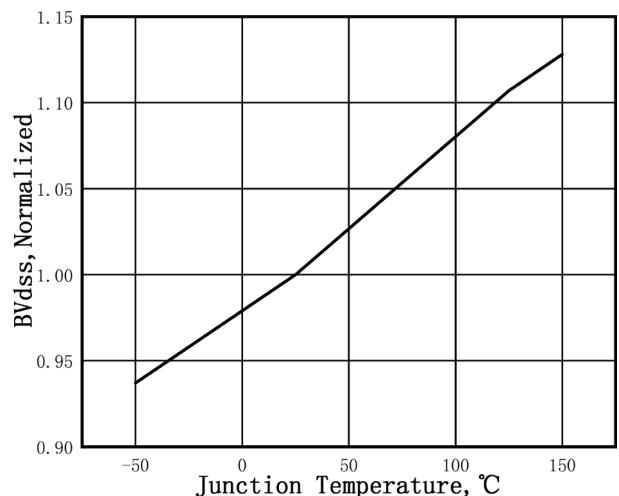


Figure 4. Typical Body Diode Transfer Characteristics

Figure 5. Normalized  $R_{DS(on)}$  vs. TemperatureFigure 6. Normalized  $BV_{DSS}$  vs. Temperature

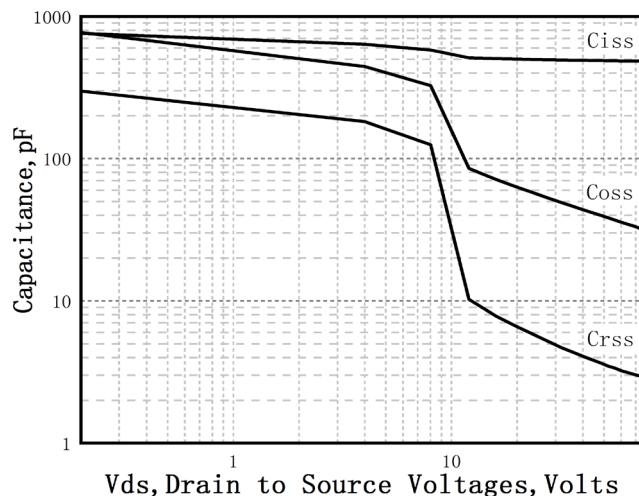


Figure 7. Capacitance Characteristics

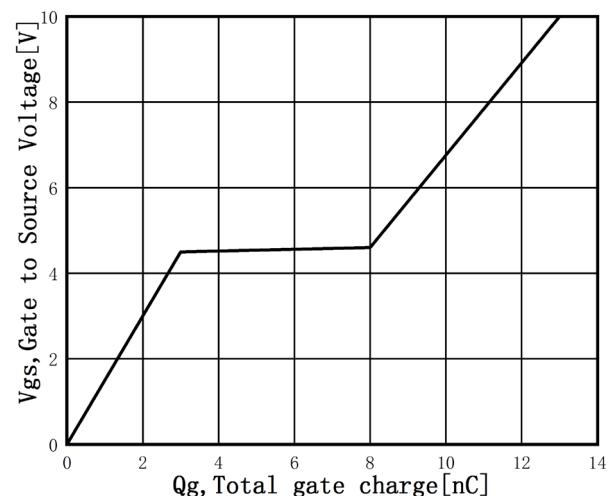
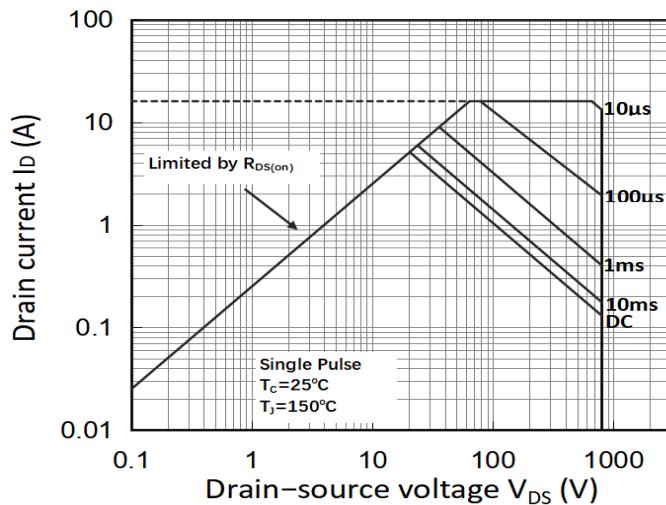
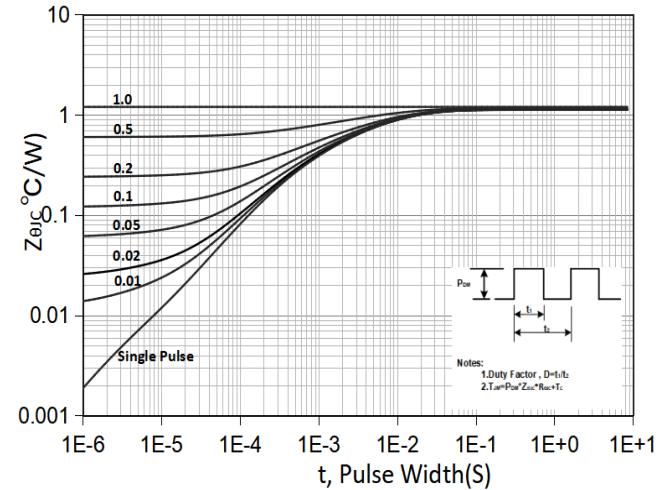
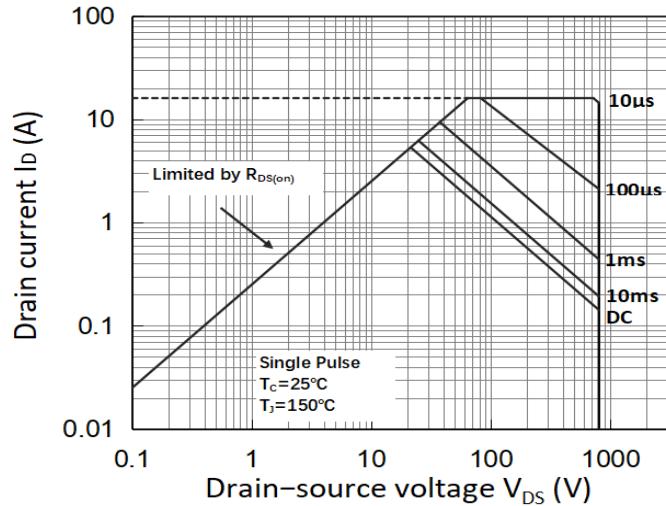
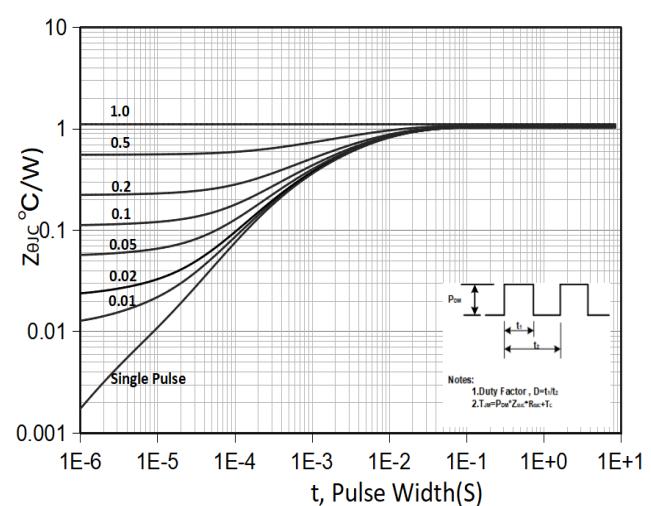
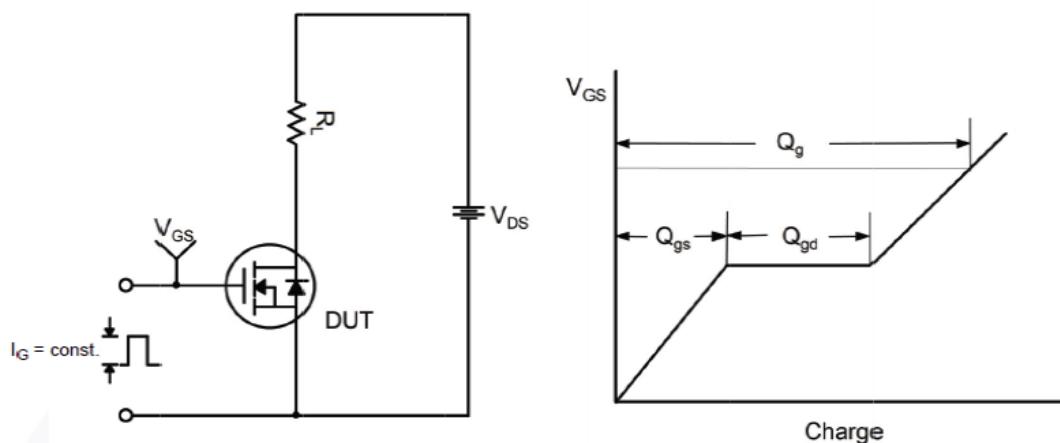


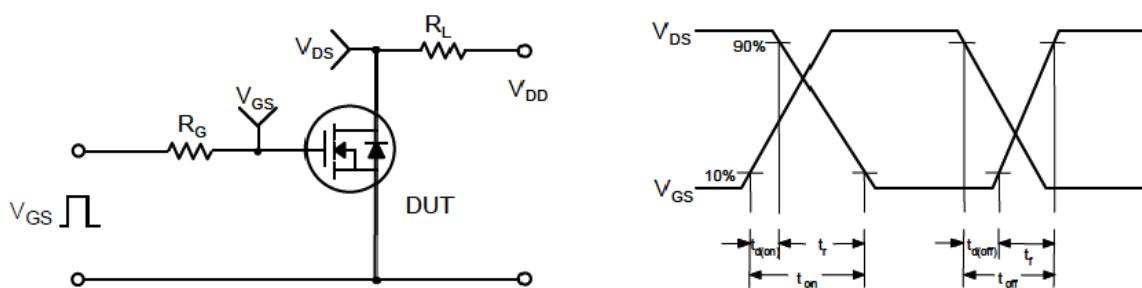
Figure 8. Gate Charge Characteristics

Figure 9. Maximum Safe Operating Area  
(TO-251/TO-252)Figure 10. Transient Thermal Response Curve  
(TO-251/TO-252)Figure 11. Maximum Safe Operating Area  
(TO-220)Figure 12. Transient Thermal Response Curve  
(TO-220)

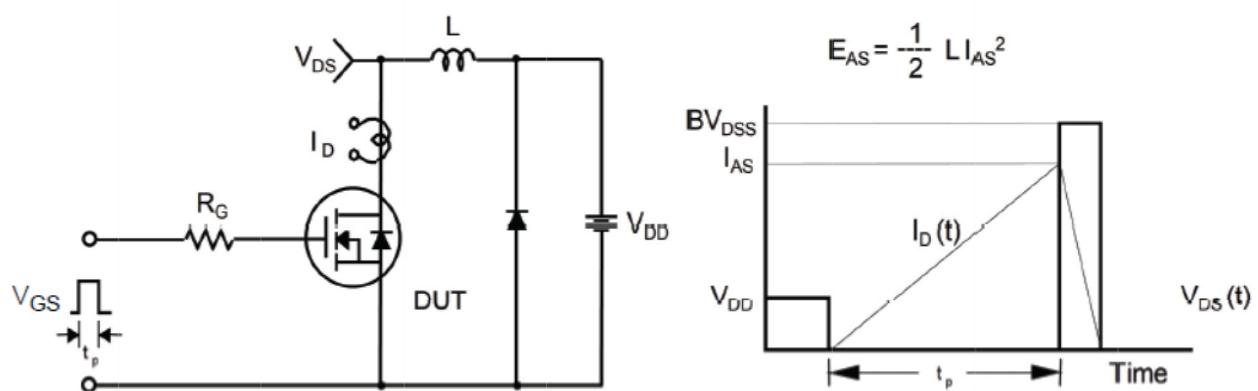
Gate Charge Test Circuit &amp;Waveform

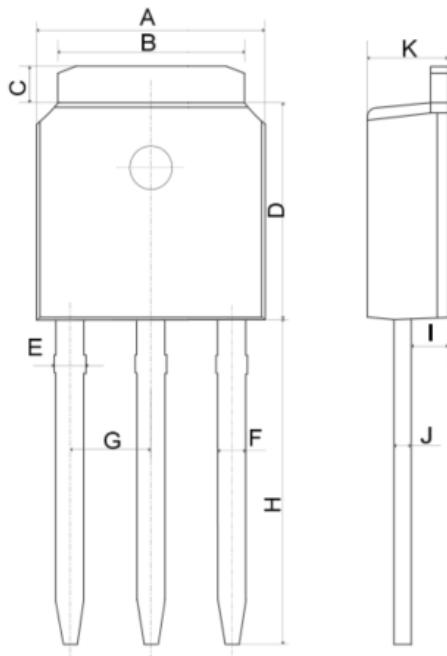


Switching Test Circuit &amp;Waveforms

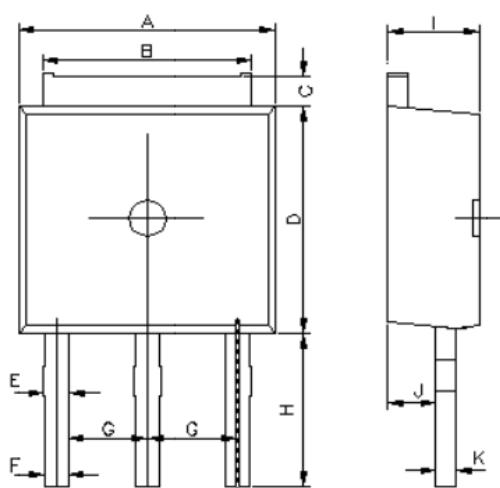


Unclamped Inductive Switching Test Circuit &amp;Waveforms

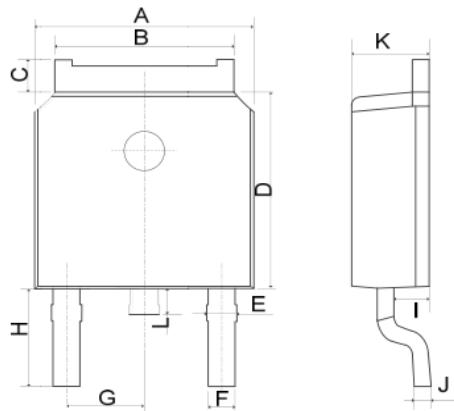


**Mechanical Dimensions for TO-251-L9.4****COMMON DIMENSIONS**

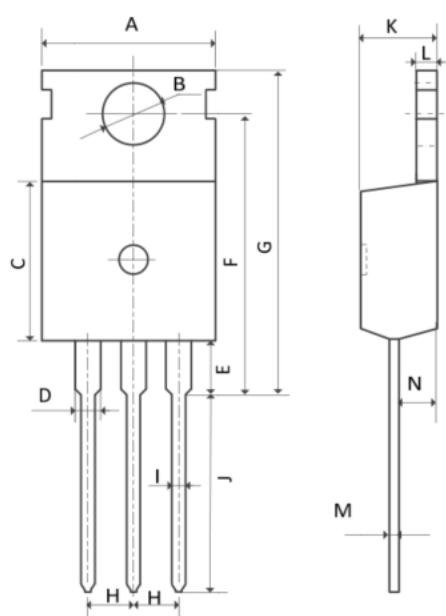
SYMBOL	MM	
	MIN	MAX
A	6.40	6.80
B	5.13	5.46
C	0.88	1.28
D	5.90	6.22
E	0.68	1.10
F	0.68	0.91
G	2.29REF	
H	9.00	9.65
I	0.90	1.17
J	0.40	0.61
K	2.10	2.50

**Mechanical Dimensions for TO-251-L3.5****COMMON DIMENSIONS**

SYMBOL	MM	
	MIN	MAX
A	6.40	6.80
B	5.15	5.48
C	0.71	1.02
D	5.95	6.35
E	0.70	1.00
F	0.70	0.90
G	2.13	2.44
H	3.20	3.80
I	2.10	2.50
J	0.85	1.15
K	0.40	0.61

**Mechanical Dimensions for TO-252****COMMON DIMENSIONS**

SYMBOL	MM	
	MIN	MAX
A	6.40	6.80
B	5.13	5.50
C	0.88	1.28
D	5.90	6.22
E	0.68	1.10
F	0.68	0.91
G	2.29REF	
H	2.90REF	
I	0.85	1.17
J	0.51REF	
K	2.10	2.50
L	0.40	1.00

**Mechanical Dimensions for TO-220****COMMON DIMENSIONS**

SYMBOL	MM	
	MIN	MAX
A	9.70	10.20
B	3.40	3.80
C	8.90	9.40
D	1.17	1.47
E	2.60	3.40
F	15.10	16.70
G	19.55MAX	
H	2.54REF	
I	0.70	0.95
J	9.35	11.00
K	4.30	4.77
L	1.20	1.45
M	0.40	0.65
N	2.20	2.60

## Ordering Information

Part	Package	Marking	Packing method
WMAA4N80D1B	TO-251-L9.4	WMAA4N80D1B	Tube
WMH4N80D1B	TO-251-L3.5	WMH4N80D1B	Tube
WMO4N80D1B	TO-252	WMO4N80D1B	Tape and reel
WMK4N80D1B	TO-220	WMK4N80D1B	Tube

## Contact Information

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